

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	449	(cache OR BTB OR BTAC) with (cycle\$1 OR stage\$1) WITH (way\$1 OR entr\$2) WITH (add\$4 OR updat\$4 OR insert\$4 OR writ\$4)	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	ON	2006/06/08 10:16
L2	93	(cache OR BTB OR BTAC) with (cycle\$1 OR stage\$1) WITH (way\$1 OR entr\$2) WITH (add\$4 OR updat\$4 OR insert\$4 OR writ\$4) WITH (select\$4 OR choos\$4 OR indicat\$4)	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	ON	2006/06/08 10:17
S1	1	NN960583	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/07 18:18
S2	41	(BTAC OR BTB) with (redundant OR ("same" NEAR2 (instruction OR branch OR address) ))	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/07 15:10
S3	308	invalidat\$4 with (redundant OR ("same" NEAR2 (instruction OR branch OR address) ))	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/07 15:10
S4	25	(invalidat\$4 with (redundant OR ("same" NEAR2 (instruction OR branch OR address) ))) SAME (branch\$4 or predict\$4)	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/07 15:15
S5	68	(invalidat\$4 with (redundant))	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/07 15:20
S6	28	((invalidat\$4 OR delete OR remove) with (duplicate OR redundant)) SAME (precict\$4 OR branch\$4)	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/07 15:27
S7	17	((discard\$4) with (duplicate OR redundant)) SAME (precict\$4 OR branch\$4)	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/07 15:28
S8	237	"same" (tag OR address) WITH (invalidat\$4 OR remov\$4 OR void\$3 OR nullif\$3)	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/07 18:57
S9	54	"matching" (tag OR address) WITH (invalidat\$4 OR remov\$4 OR void\$3 OR nullif\$3)	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/07 19:01

## EAST Search History

S10	151	("same" OR "matching") (entry) WITH (invalidat\$4 OR remov\$4 OR void\$3 OR nullif\$3)	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/07 19:01
S11	26	((("same" OR "matching") (entry) WITH (invalidat\$4 OR remov\$4 OR void\$3 OR nullif\$3)) SAME (predict\$3 OR branch\$3 OR BTAC OR BTB)	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/07 19:08
S12	31	((("same" OR "matching") (tag OR address OR target OR instruction) WITH (invalidat\$4 OR remov\$4 OR void\$3 OR nullif\$3)) SAME (predict\$3 OR branch\$3 OR BTAC OR BTB)	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/07 19:08
S13	0	"apparatus and method for invalidation of redundant"	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/10 17:12
S14	1	(apparatus AND method AND invalidation AND redundant).TI.	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/10 18:40
S15	771	invalidat\$4 request\$1	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/10 17:42
S16	0	(invalidat\$4 request\$1) WITH BTAC	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/10 17:42
S17	0	(invalidat\$4 request\$1) SAME BTAC	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/10 17:42
S18	0	(invalidat\$4 request\$1) SAME BTB	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/10 17:42
S19	3	(invalidat\$4 NEAR4 request\$1) SAME BTAC	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/10 17:43
S20	3	(invalidat\$4 WITH request\$1) SAME BTAC	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/10 17:43

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S21	0	(invalidat\$4 WITH request\$1) SAME BTB	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/10 17:43
S22	0	(invalidat\$4 WITH request\$1) SAME BHT	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/10 17:43
S23	47	(invalidat\$4 WITH request\$1) SAME (set associat\$4)	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/10 17:45
S24	8	(invalidat\$4 NEAR5 request\$1) SAME (fetch address)	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/10 17:45
S25	8	(invalidat\$4 NEAR5 request\$1) SAME (fetch address)	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	ON	2006/03/10 17:50
S26	18	(invalidat\$4) WITH (fetch address)	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	ON	2006/03/10 18:07
S27	37	BTAC with cycle\$1	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	ON	2006/03/10 18:08
S28	14	BTAC with cycle\$1 WITH miss	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	ON	2006/06/08 10:15
S29	11	BTAC WITH miss WITH stage	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	ON	2006/03/10 18:10
S30	0	BT WITH miss WITH stage	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	ON	2006/03/10 18:10
S31	2	BTB WITH miss WITH stage	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	ON	2006/03/10 18:10
S32	3	BTB SAME miss WITH stage	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	ON	2006/03/10 18:25

## EAST Search History

S33	2063	short loops	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	ON	2006/03/10 18:25
S34	70	(short loops) WITH branch	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	ON	2006/03/10 18:26
S35	21	(short loops) WITH predict\$4	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	ON	2006/03/10 18:26
S36	11	LRU WITH BTB	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/10 18:45
S37	0	miss WITH (BTB OR BTAC) WITH way	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/10 18:46
S38	57	miss WITH (BTB OR BTAC)	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/10 18:54
S39	37	update WITH (BTB OR BTAC) WITH address	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/10 18:54
S40	18	update WITH (BTB OR BTAC) WITH way	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/10 18:59
S41	0	set\$1 WITH valid WITH reslov\$4	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/10 19:00
S42	65	set\$1 WITH valid WITH resolv\$4	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/10 19:09
S43	5278	LRU	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/10 19:10
S44	537	LRU WITH only	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/10 19:10

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S45	6	LRU WITH only WITH branch	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	ON	2006/03/10 19:14
S46	123	LRU WITH predict\$4	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	ON	2006/03/10 19:15
S47	0	LRU WITH when WITH predict\$4	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	ON	2006/03/10 19:15
S48	6	LRU WITH read WITH predict\$4	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	ON	2006/03/10 19:15
S49	84	(set associative) WITH advantage	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	ON	2006/03/11 16:24
S50	16329	upper WITH index	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/13 11:26
S51	0	upper WITH index WITH BTAC	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/13 11:26
S52	92	upper WITH index WITH cache	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/13 11:26
S53	3	upper WITH index WITH set associative	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/13 11:27
S54	25	upper WITH index SAME set associative	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/13 11:27

**Jun 8, 2006**

**BTC OR BTB OR NEAR2 instruction OR branch OR address**

- ☆ Personal Computer CHIPLIST 9.9.4 part 4 of 5 - 10:35am  
[www.faqs.org/faqs/pc-hardware-faq/chiplist/part4/](http://www.faqs.org/faqs/pc-hardware-faq/chiplist/part4/)
- ☆ BTB (Branch target buffer) - 10:35am  
[csd.ijs.si/courses/processor/Chapter1/tsld059.htm](http://csd.ijs.si/courses/processor/Chapter1/tsld059.htm)

**BTC OR BTB instruction OR branch OR address**

- ☆ BTB - Branch Target Buffer - 10:35am  
[www.auditmypc.com/acronym/BTB.asp](http://www.auditmypc.com/acronym/BTB.asp)
- ☆ Branch delay slot - Wikipedia, the free encyclopedia - 10:35am  
[en.wikipedia.org/wiki/Branch\\_delay\\_slot](http://en.wikipedia.org/wiki/Branch_delay_slot)
- ☆ Evaluation of branch-prediction methods on traces from commercial ... - 10:35am  
[www.research.ibm.com/journal/rd/434/hilgendorf.html](http://www.research.ibm.com/journal/rd/434/hilgendorf.html)

**cache OR BTB OR BTAC cycle OR stage way OR entry add OR update OR insert OR write select OR choose OR indicate**

- ☆ CS/ECE 552: Introduction to Computer Architecture - 10:35am  
[www.cs.wisc.edu/~cs552-1/Project/cachemem.html](http://www.cs.wisc.edu/~cs552-1/Project/cachemem.html)
- ☆ Lab #4: Cache Simulation Experiments - 10:35am  
[www-cse.ucsd.edu/classes/wi06/cse141L/lab4.pdf](http://www-cse.ucsd.edu/classes/wi06/cse141L/lab4.pdf)

**cache |BTB | BTAC cycle OR stage way OR entry add OR update OR insert OR write select OR choose OR indicate**

- ☆ Computing Cache Vulnerability to Transient Errors and Its Implication - 10:35am  
[doi.ieeecomputersociety.org/10.1109/DFTVS.2005.23](http://doi.ieeecomputersociety.org/10.1109/DFTVS.2005.23)
- ☆ CPU cache - Wikipedia, the free encyclopedia - 10:35am  
[en.wikipedia.org/wiki/L3\\_cache](http://en.wikipedia.org/wiki/L3_cache)

**cache | BTB | BTAC cycle OR stage way OR entry add OR update OR insert OR write select OR choose OR indicate**

- ☆ Cache experiment - 10:34am  
[ocw.mit.edu/.../0/cacheexperiment\\_answers.pdf](http://ocw.mit.edu/.../0/cacheexperiment_answers.pdf)